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We propose mathematical methods aimed at formalizing the computer implementation of arithmetic computations. A model-theoretic method of partial interpretation is elaborated for constructing formal specifications of computation models which takes resource limitations into account. Using this method we produce and analyze various integer and rational computation models including, in particular, the positional number systems. Architecture models of the arithmetic are created based on the language of the finite-valued Łukasiewicz logic and logics that enrich it. The weak completeness of such logics enabled us to research into the structural characteristics of operations independent of the representation of numbers. In particular, the mechanisms of detection and processing overflows have been analyzed. Various computation models are represented as bases of logic functions. A technique is proposed for verifying the absence of overflow in computing arithmetic expressions by proving many-valued logic theorems.

Key words and phrases: computer arithmetic, partial interpretation, Łukasiewicz logic, overflow, carry flag.

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